## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

## Electronic Version v18

Stylesheet Version v18.0

Title of Invention

Dynamic PCI-Bus Pre-Fetch with Separate Counters for Commands of Different Data-Transfer Lengths

**Application Number:** 

Confirmation Number:

First Named Applicant:

Baohua Chen

Attorney Docket Number:

PS-102

Art Unit:

Examiner:

Search string:

( 6578102 or 6557080 or 6502157 or 6385641 or 6381679 or 6286074 or 6134643

or 5983306 or 5941981 or 5761464 or 5659713 or 5146578 or 4621320 or 4489378

or 6178483).pn

## **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
BTM	1	6578102	2003-06-10	Batchelor, et al.	B1	710	310
BTM	2	6557080	2003-04-29	Burger, et al.	B1	711	137
BTM	3	6502157	2002-12-31	Batchelor, et al.	B1	710	310
BTM	4	6385641	2002-05-07	Jiang , et al.	B1	709	203
BTM	5	6381679	2002-04-30	Matsubara , et al.	B1	711	137
BTM	6	6286074	2001-09-04	Batchelor, et al.	B1	710	305
BIM	7	6134643	2000-10-17	Kedem, et al.	B1	711	213
BTM	8	5983306	1999-11-09	Corrigan , et al.	B1	710	310
BTM	9	5941981	1999-08-24	Tran	B1	712	207
BTM	10	5761464	1998-06-02	Hopkins	B1	710	310
BTW	11	5659713	1997-08-19	Goodwin , et al.	B1	711	157
BTW	12	5146578	1992-09-08	Zangenehpour	B1	711	137
BTM	13	4621320	1996-11-04	Holste , et al.	B1	711	213
BTM	14	4489378	1984-12-18	Dixon , et al.	B1	710	33
Bim	15	6178483	2001-01-23	Runaldue, et al.	B1	711	137

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Examiner Name	Date
Brian T. Misiura	3/6/2006